

**Amendments to the Specification:**

Please amend the specification as follows:

**Page 5, lines 6-12:**

A problem arises when the ~~users~~ user wishes to access a session but does not have the physical token associated with the session. In the examples above, the user may discover upon reaching the cafeteria that the physical token is still in the user's office or the user may discover upon returning to work that the physical token is still at the user's home. In prior art systems, the user is unable to access the desired session. Instead, the user must either retrieve the physical token or locate a system administrator who can enable the user to access the session.

**Page 14, lines 21—27 to page 15, lines 1-15:**

A block diagram of an example embodiment of the HID is illustrated in Figure 8. The components of the HID are coupled internally to a PCI bus 812. Network control block 802 communicates to the interconnect fabric, such as an Ethernet, through line 814. An audio codec 803 receives audio data on interface 816 and is coupled to network control block 802. USB data communication is provided on lines 813 to a USB controller 801. The HID further comprises ~~[[a]]~~ an embedded processor 804 such as a Sprac2ep with coupled flash memory 805 and DRAM 806. The USB controller 801, the network control block 802 and the embedded processor 804 are all coupled to the PCI bus 812. A video controller 809, also coupled to the PCI bus 812, can include an ATI Pagepro<sup>+</sup> frame buffer controller which provides SVGA output on the line 815. NTSC data is provided in and out of the video controller through video decoder 810 and encoder 811 respectively. A smartcard interface 808 may also be coupled to the video controller 809.

Alternatively, the HID can comprise a single chip implementation as illustrated in Figure 9. The single chip includes the necessary processing capability implemented via CPU 901 and graphics renderer 905. Chip memory 907 is provided, along with video controller/interface 906. [[A]] An internal bus (USB) controller 902 is provided to permit communication to a mouse, keyboard and other local devices attached to the HID. A sound controller 903 and interconnect interface 904 are also provided. The video interface shares memory 907 with the CPU 901 and graphics renderer 905. The software used in this embodiment may reside locally in on-volatile memory or it can be loaded through the interconnection interface when the device is powered.